

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

PATENT

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In re Patent Application of: Hoon LEE, et al.

8-27-01

Serial No.: New Application Group Art Unit: Not Yet Assigned

Filed: May 2, 2001

Examiner: Not Yet Assigned

Title: VARIABLE-RATE QAM TRANSCEIVER.

J1046 U.S. PTO
09/846205
05/02/01

INFORMATION DISCLOSURE STATEMENT

Honorable Commissioner for Patents
Washington, DC 20231

May 2, 2001

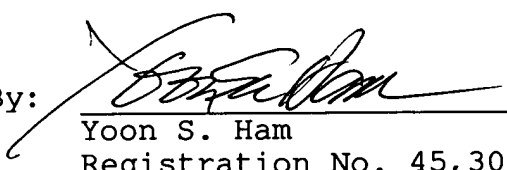
Sir:

As a means of complying with the duty of disclosure under 37 CFR §1.56, and in accordance with 37 CFR §§1.97 and 1.98, Applicant(s), through the undersigned attorney, submits this Information Disclosure Statement. The patents, publications or other information submitted herewith are listed on the attached Form PTO-1449 and copies are attached.

In accordance with 37 CFR §1.97(b)(1) or (2), this Information Disclosure Statement is being filed either within three months of the filing date of the above-identified application, or within three months of the date of entry into the national stage of the above-identified application as set forth in 37 CFR §1.491. Accordingly, no fee is required.

Respectfully submitted,
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LIST OF PATENTS AND PUBLICATIONS FOR APPLICANT'S INFORMATION DISCLOSURE STATEMENT

ATTY. DOCKET NO.: P66632US0 GROUP ART UNIT: Not Yet Assigned
 SERIAL NO.: Not Yet Assigned FILING DATE: May 2, 2001
 APPLICANT(S): Hoon LEE, et al. TODAY'S DATE: May 2, 2001

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U.S. PATENT DOCUMENTS

*EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	INT'L CLASS	SUB- CLASS	FILING DATE (If Appropriate)
AA	6,144,712	11/07/00	Samueli et al.			10/09/97
AB						
AC						

FOREIGN PATENT DOCUMENTS

	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB- CLASS	TRANSLATION (YES) (NO)
AD						
AE						

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

AF Samueli et al.; VLSI Architectures for a High-Speed tunable Digital Modulator/Demodulator/Bandpass-Filter Chip Set; Integrated Circuits and Systems Laboratory Electrical Engineering Department, University of California, Los Angeles; 1992 IEEE, Pages 1065-1068.

AG D'Luna et al.; A Single-Chip Universal Code Set-Top Box/Modem Transceiver; IEEE Journal of Solid-State Circuits, Vol. 34, No. 11, November 1999, Pages 1647-1660.

EXAMINER

DATE CONSIDERED

* EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant(s).

Information Disclosure Statements

U.S. Patent Application for VARIABLE-RATE QAM TRANSCEIVER
Our Ref. No. P00EC006/US/jw

Reference No. : US 6144712

Papers :

(1) VLSI ARCHITECTURES FOR A HIGH-SPEED TUNABLE DIGITAL
MODULATOR/DEMODULATOR/BANDPASS-FILTER CHIP SET

(2) A SINGLE-CHIP UNIVERSAL CABLE SET-TOP BOX/MODEM
TRANSCEIVER